

BEST AVAILABLE COPY

Fig. 1

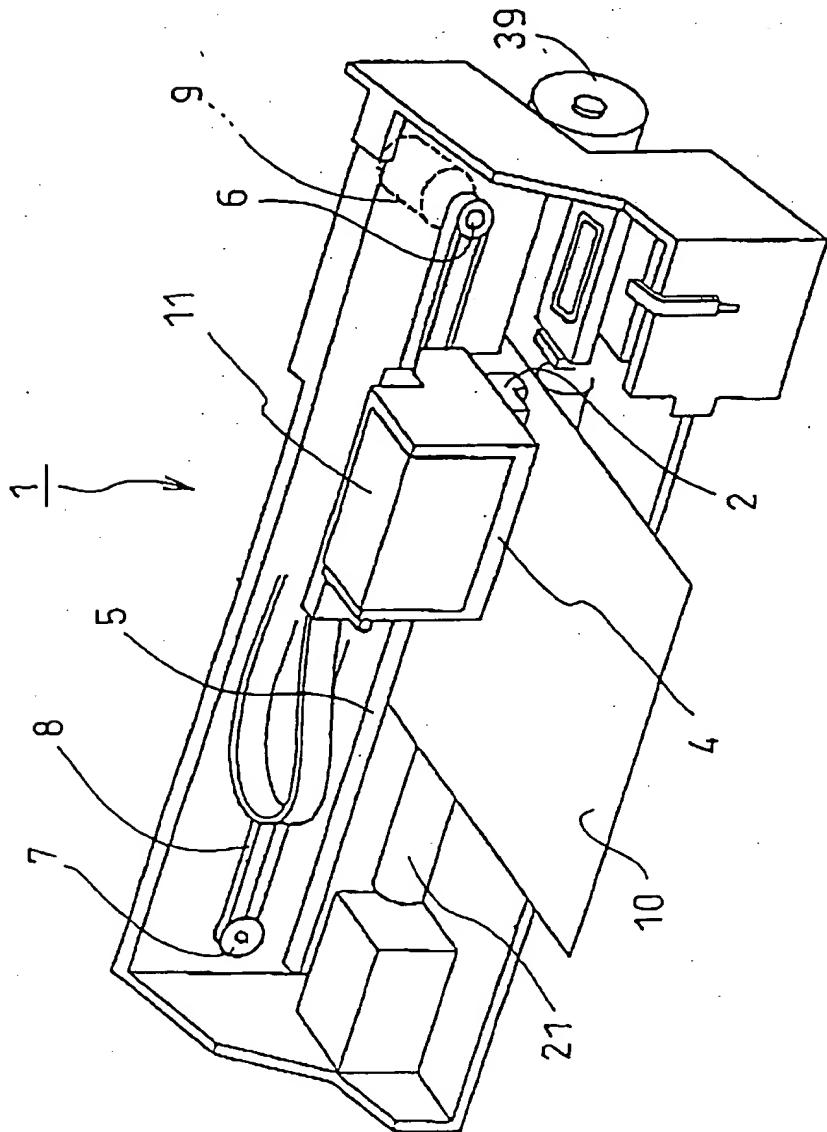


Fig. 2

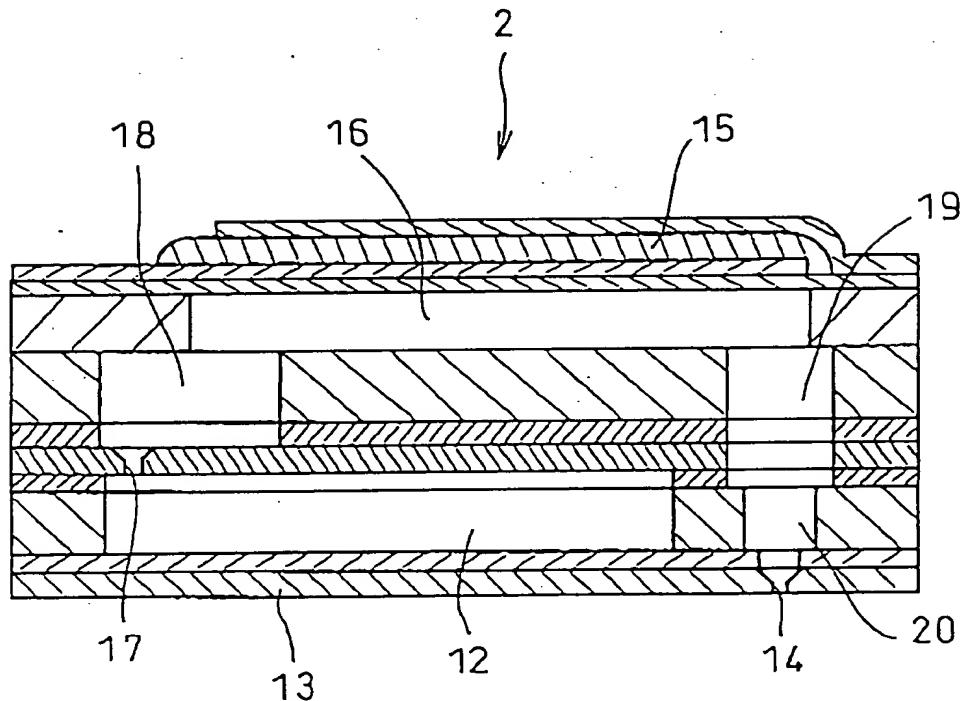
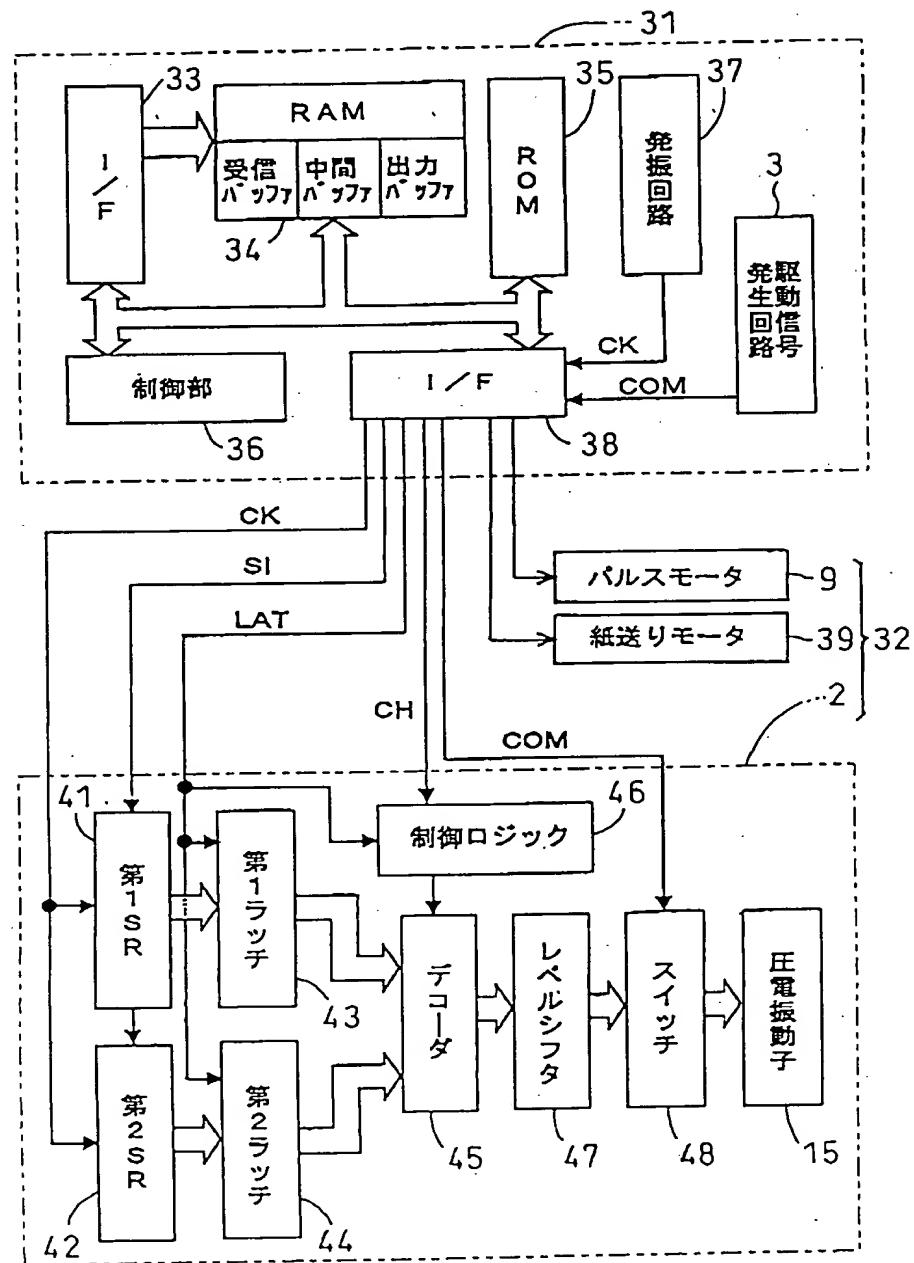
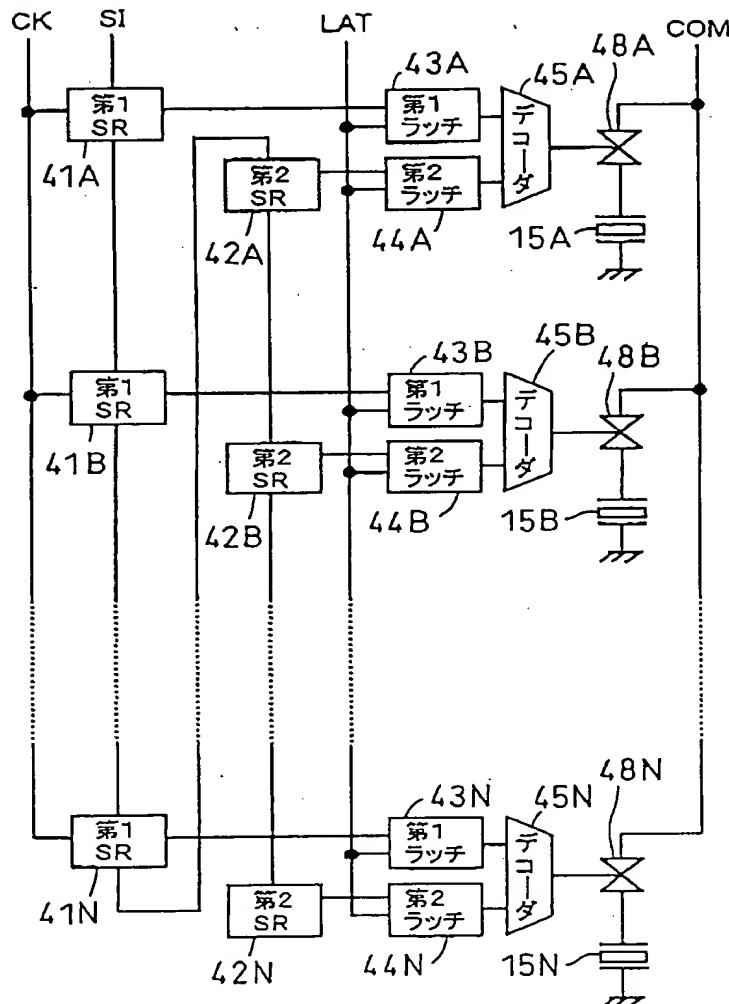


Fig. 3



- | | |
|--|-----------------------------------|
| 3: drive signal generator | 41: first shift register section |
| 9: pulse motor | 42: second shift register section |
| 15: piezoelectric vibrator | 43: first latching section |
| 33: external I/F | 44: second latching section |
| 34: reception buffer, intermediate buffer, output buffer | 45: decoder |
| 36: controller | 46: control logic |
| 37: oscillator | 47: level shifter |
| 38: internal I/F | 48: switcher |
| 39: paper feeding motor | |

Fig. 4



- 41A ... 41N: first shift register element
42A ... 42N: second shift register element
43A ... 43N: first latching element
44A ... 44N: second latching element
45A ... 45N: decoder element

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Fig. 5

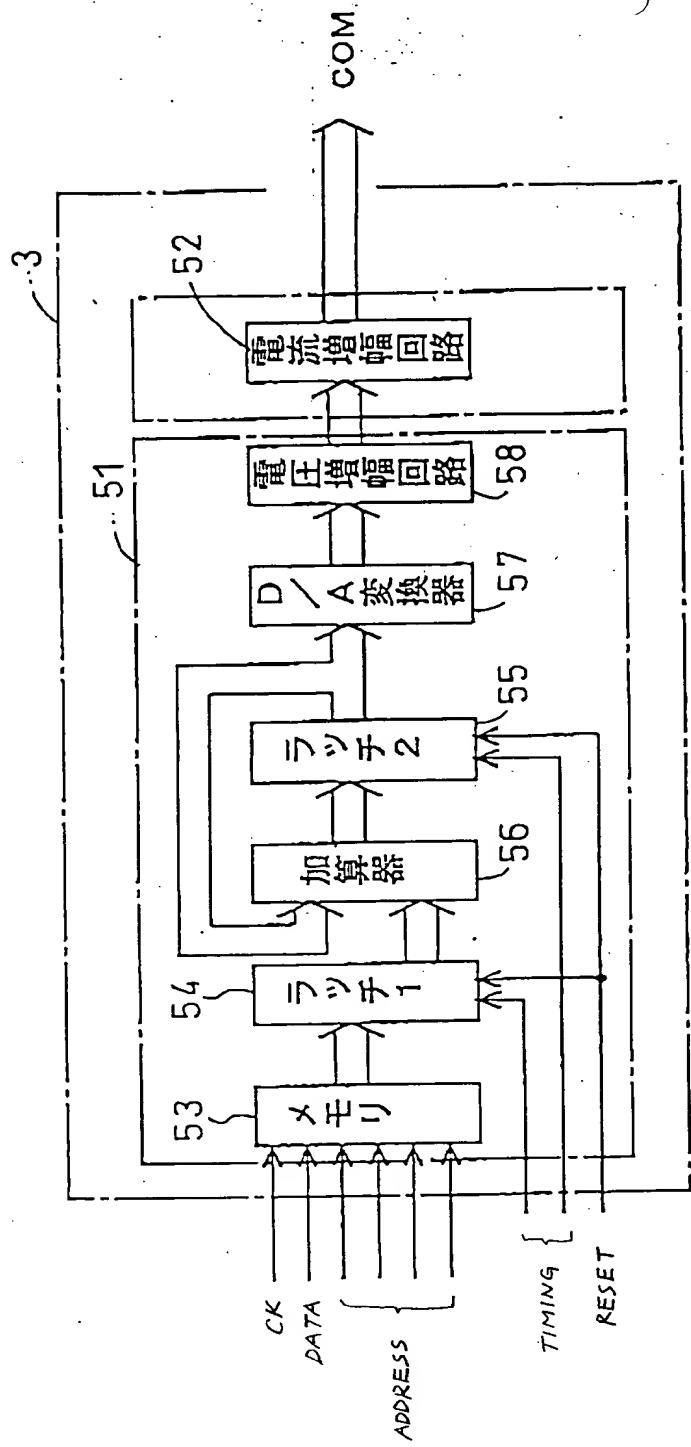


Fig. 6

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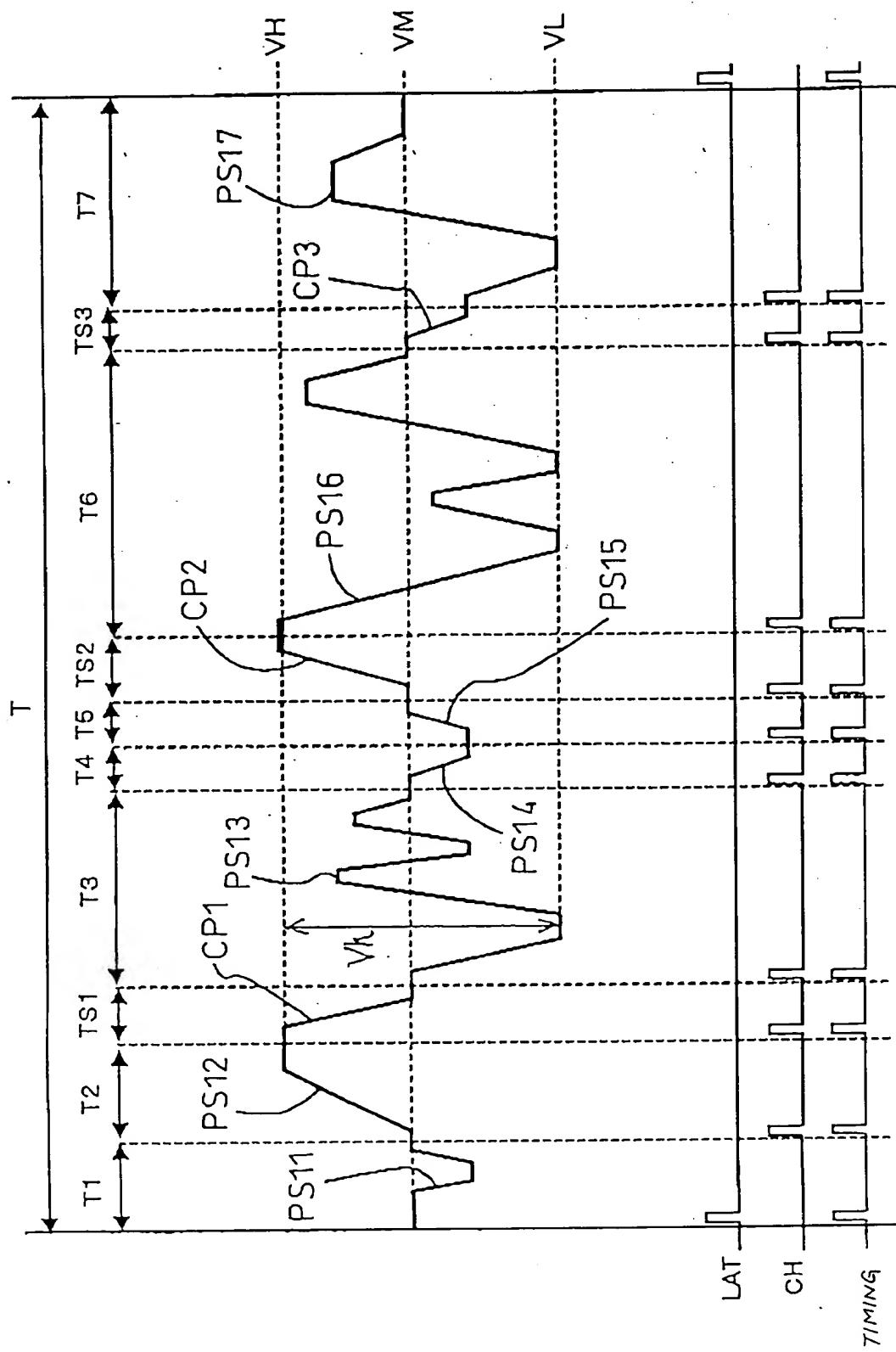
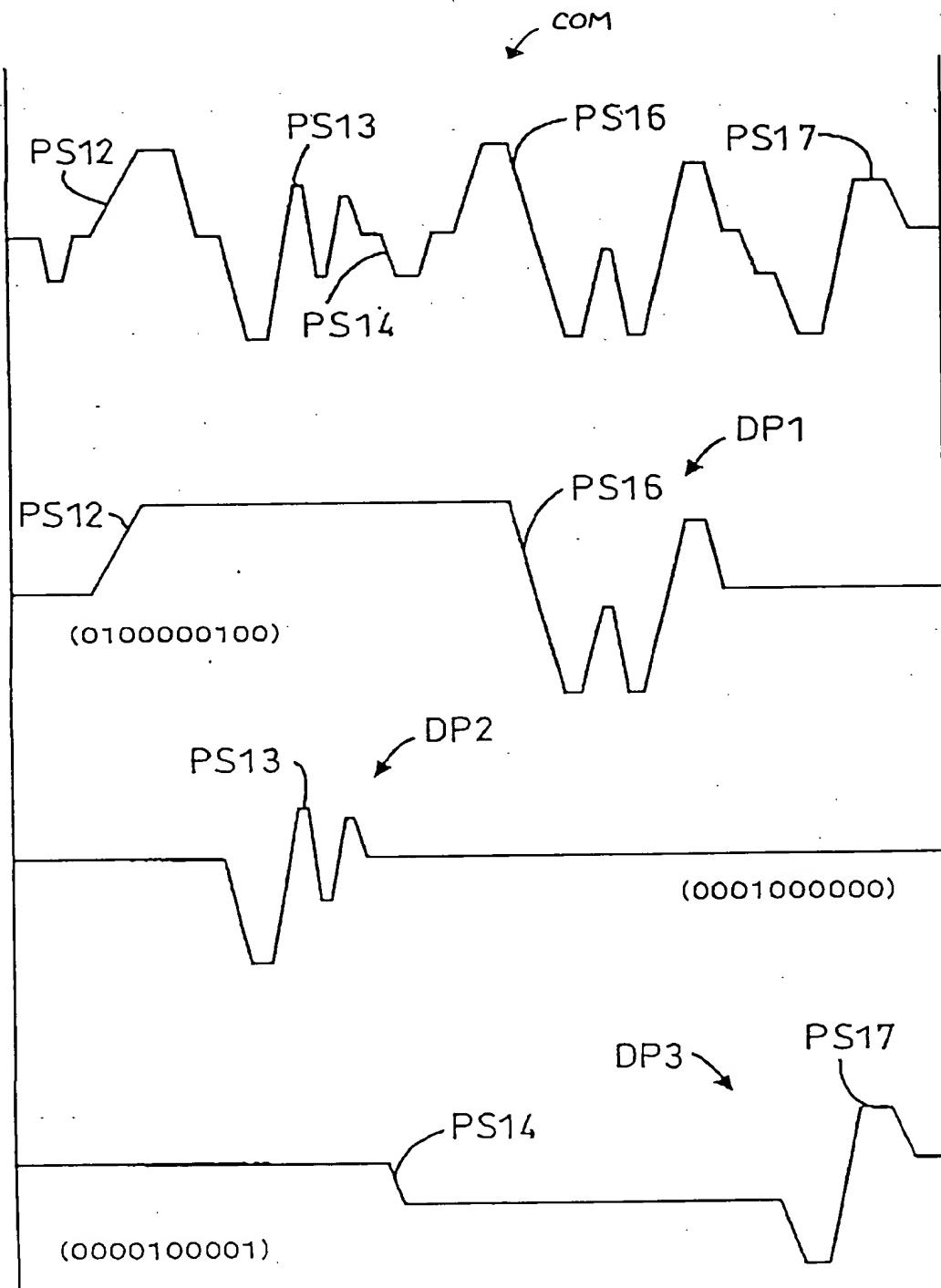


Fig. 7



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Fig. 8

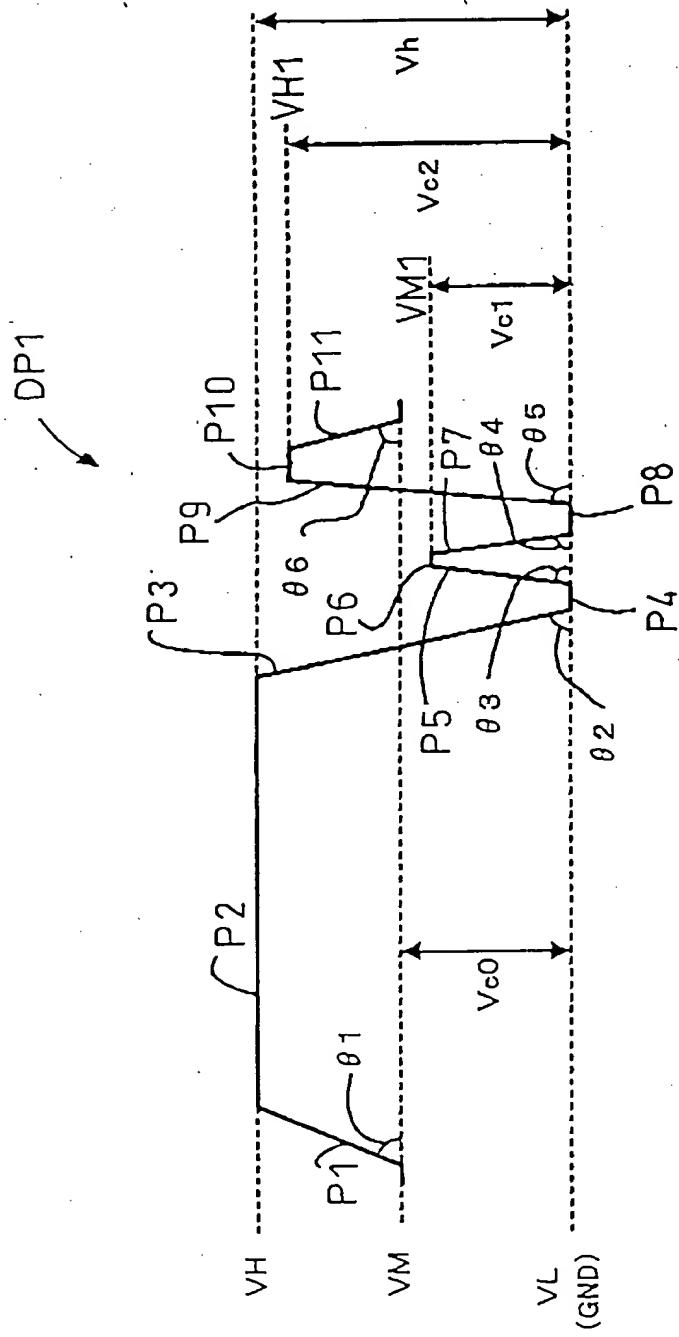


Fig. 9A

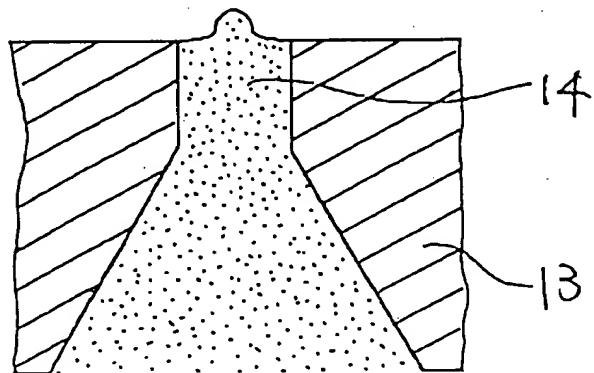


Fig. 9B

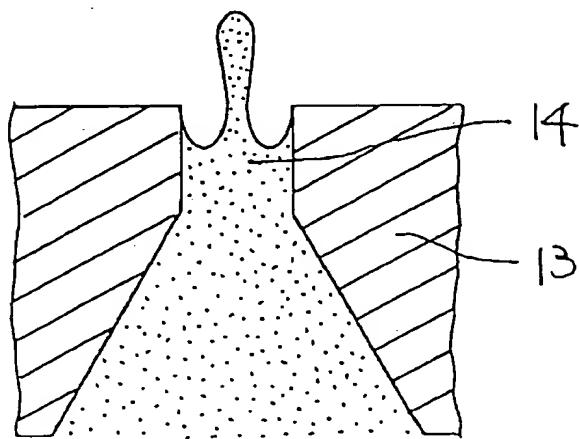
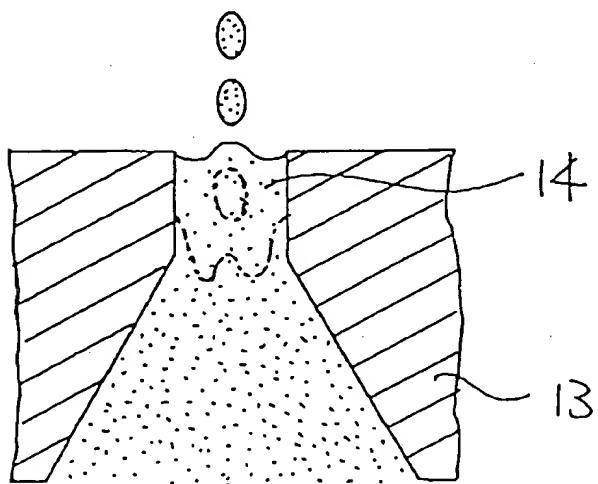


Fig. 9C



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Fig. 10

